Application No. 10826814 (Docket: CNTR.2226)
37 CFR 1.111 Amendment dated 01/10/2008
Reply to Office Action of 09/28/2007

AMENDMENTS TO THE ABSTRACT

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety
and substitute the following section therefor:

ABSTRACT OF THE DISCLOSURE

The present invention provides an apparatus and method for performing cryptographic
operations on a plurality of input data blocks within a processor. In one embodiment, an
apparatus for performing cryptographic operations is provided. The apparatus includes a
cryptographic instruction, CBC block pointer logic, and execution logic. The
cryptographic instruction is received by a microprocessor ~~computing device~~ as part of an
instruction flow executing on the microprocessor~~computing device~~. The cryptographic
instruction prescribes one of the cryptographic operations. The one of the cryptographic
operations includes a plurality of CBC block cryptographic operations performed on a
corresponding plurality of input text blocks. The CBC block pointer logic is operatively
coupled to the cryptographic instruction. The CBC block pointer logic directs the
microprocessor ~~computing device~~ to update pointer registers and intermediate results for
each of the plurality of CBC block cryptographic operations. The execution logic is
operatively coupled to the CBC block pointer logic. The execution logic executes the
one of the cryptographic operations.